Attorney's Docket No.:10559-567001

instructions stored therein to an execution unit 220 which returns a result back to the processor core 208. However, it is respectfully submitted that a memory access would not be routed through the execution unit 220.

Palanca's, arithmetic logic unit (ALU) instructions are dispatched from the processor core to the execution unit 220 (see, inter alia, Palanca col. 6, lines 58-60). The execution unit 220 executes the ALU instructions and returns results back to a reorder buffer and register file 215 within the processor core 208 (see, inter alia, Palanca col. 6, lines 60-63).

Memory instructions, on the other hand, are dispatched to a memory ordering unit 230 which resides within a memory cluster 225 (see, inter alia, Palanca col. 6, lines 63-66). The memory ordering unit 230 dispatches instructions to a L1 cache controller 235 which may result in data being transferred to the processor core 208 (see, inter alia, Palanca col. 7, lines 4-52).

It is reasonable to assume that a memory access is a specific type of memory instruction. Palanca's teaching is that its memory access should be routed through the memory cluster 225. Therefore, Palanca fails to disclose an arrangement that does not traverse memory external to the processor core.

Accordingly, claims 1, 15, 30, and their respective dependent claims should be allowable.

It is believed that all of the pending claims have been addressed in this paper. However, failure to address a specific rejection, issue or comment, does not signify agreement with or concession of that rejection, issue or comment. In addition, because the arguments made above are not intended to be exhaustive, there may be reasons for patentability of any or all pending claims (or other claims) that have not been expressed. Finally, nothing in this paper should be construed as an intent

Attorney's Docket No.:10559-567001

to concede any issue with regard to any claim, except as specifically stated in this paper, and the amendment of any claim does not necessarily signify concession of unpatentability of the claim prior to its amendment.

Applicant asks that all claims be allowed. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 7 (05

Scott C. Harris Req. No. 32,030

Attorney for Intel Corporation

Fish & Richardson P.C.

PTO Customer Number: 20985

12390 El Camino Real San Diego, CA 92130

Telephone:

(858) 678-5070

Facsimile: (858) 678-5099

10515382.doc